



SHEET 1 OF 1

Form PTO 1449
(Modified)U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICEATTY DOCKET NO.
240262US2SSERIAL NO.
10/617,400

LIST OF REFERENCES CITED BY APPLICANT

APPLICANT
Kenichi IMAMIYA, et al.FILING DATE
July 11, 2003GROUP
2818

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						
	AM						
	AN						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION	
					YES	NO
cy	AO	2001-176290	06/29/2001	JAPAN		X
	AP					
	AQ					
	AR					
	AS					
	AT					
	AU					
	AV					

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)

cy	AW	K. IMAMIYA, et al., IEEE Journal of Solid-State Circuits, vol. 34, no. 11, pages 1536-1543, "A 130-mm ² , 256-Mbit NAND FLASH WITH SHALLOW TRENCH ISOLATION TECHNOLOGY", November 1999				
cy	AX	K-D. SUH, et al., ISSCC95 / SESSION 7 / FLASH MEMORY / PAPER TA 7.5, Digest of Technical Papers, pages 128-129, "A 3.3V 32Mb NAND FLASH MEMORY WITH INCREMENTAL STEP PULSE PROGRAMMING SCHEME", February 16, 1995				
	AY					
	AZ					

☐ Additional References sheet(s) attached

Examiner

Date Considered

5/04

*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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New Application

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FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION	
					YES	NO
cy	AO	2002-117699	4/19/02	Japan		X
	AP					
	AQ					
	AR					
	AS					
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	AU					
	AV					

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)

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